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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/622,278	07/18/2003	Ga-pyo Nam	5649-1102	4592
20792	7590 01/05/2005		EXAMINER	
MYERS BIGEL SIBLEY & SAJOVEC PO BOX 37428			LAM, DAVID	
RALEIGH, 1	-		ART UNIT	PAPER NUMBER
·	,		2818	
			DATE MAIL ED. 01/05/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

		AL
	Application No.	Applicant(s)
	10/622,278	NAM ET AL.
Office Action Summary	Examiner	Art Unit
	David Lam	2818
The MAILING DATE of this communication ap Period for Reply	pears on the cover sheet with the	correspondence address
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a rep If NO period for reply is specified above, the maximum statutory period Failure to reply within the set or extended period for reply will, by statut Any reply received by the Office later than three months after the mailine earned patent term adjustment. See 37 CFR 1.704(b).	136(a). In no event, however, may a reply be timely within the statutory minimum of thirty (30) day will apply and will expire SIX (6) MONTHS from the cause the application to become ABANDONE.	nely filed ys will be considered timely. Ithe mailing date of this communication. ED (35 U.S.C. § 133).
Status		
Responsive to communication(s) filed on  2a) ☐ This action is FINAL. 2b) ☑ This  3) ☐ Since this application is in condition for allowed closed in accordance with the practice under the practice.	s action is non-final. ance except for formal matters, pr	
Disposition of Claims		
4)  Claim(s) 1-28 is/are pending in the application 4a) Of the above claim(s) is/are withdra 5)  Claim(s) 16-28 is/are allowed. 6)  Claim(s) 1 and 3 is/are rejected. 7)  Claim(s) 2 and 4-15 is/are objected to. 8)  Claim(s) are subject to restriction and/o Application Papers  9)  The specification is objected to by the Examina 10)  The drawing(s) filed on 18 July 2003 is/are: a applicant may not request that any objection to the Replacement drawing sheet(s) including the correction.	er.  accepted or b) objected to be drawing(s) be held in abeyance. Section is required if the drawing(s) is objected to be compared to be drawing(s) be held in abeyance.	e 37 CFR 1.85(a). jected to. See 37 CFR 1.121(d).
Priority under 35 U.S.C. § 119		·
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of:  1. Certified copies of the priority documen 2. Certified copies of the priority documen 3. Copies of the certified copies of the priority documen application from the International Burea * See the attached detailed Office action for a list	ts have been received. ts have been received in Applicat prity documents have been receiv nu (PCT Rule 17.2(a)).	ion No ed in this National Stage
Attachment(s)	_	
<ol> <li>Notice of References Cited (PTO-892)</li> <li>Notice of Draftsperson's Patent Drawing Review (PTO-948)</li> <li>Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08 Paper No(s)/Mail Date 7/18/03.</li> </ol>	4) Interview Summary Paper No(s)/Mail D 5) Notice of Informal I 6) Other:	

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#### **DETAILED ACTION**

#### **Drawings**

- 1. The drawings are objected to as failing to comply with 37 CFR 1.84(p)(5) because they do not include the following reference sign(s) mentioned in the description: 318 on page 7, line 13; 510 on page 9, line 9. A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.
- 2. The drawings are objected to as failing to comply with 37 CFR 1.84(p)(5) because they include the following reference sign(s) not mentioned in the description: 500 on Figure 5; 611 on Figure 6. A proposed drawing correction, corrected drawings, or amendment to the specification to add the reference sign(s) in the description, are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.

## Specification

3. The disclosure is objected to because of the following informalities: On page 10, line 16, '605' should be change to -- 600 -- in order to be consistent with feature shown on Figure 6.

Appropriate correction is required.

# Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

4. Claims 1, 3 are rejected under 35 U.S.C. 102(b) as being anticipated by Hung (5,886,923).

Regarding to claims 1, 3, Hung discloses a flash memory device comprising: a local row decoder (308) configured to drive word lines coupled to a sector of flash memory responsive to separate read and write control signals (A1, A2, CTLA, CTLB, Vps, Vns) provide thereto from outside the local row decoder circuit; a global row decoder circuit (302) coupled to the local row decoder circuit via the separate read and write control signal, and configured to activate the separate read and write control signals base on an address provided to the global row decoder circuit that indicates memory cells associated with the address are accessed using the word lines. See Figs. 3-5; Cols. 6-11.

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### Allowable Subject Matter

5. Claims 2, 4-15 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The following is a statement of reasons for the indication of allowable subject matter:

The prior art of record fails to teach wherein the above noted flash memory support read-whilewrite capability that performing read operation in a first bank of the flash memory can be carried
out simultaneous with a write operation in a second bank of the flash memory.

6. The following is an examiner's statement of reasons for allowance: Claims 16-28 are allowable over the prior art of record because none of the prior art whether taken singularly or in combination, especially when these limitations are considered within the specific combination claimed, to teach: a flash memory device comprising: local row decoders circuit, among others as claimed in independent claim 16, in respond to a separate write and read control signals, to enable read operation in a first sector in one of a plurality of banks and enables a write operation in a second in another of the plurality of the of banks simultaneous with the read operation.

#### Conclusion

- 7. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.
- Kobayashi et al. (5,898,606) disclose an electrically programmable and erasable nonvolatile semiconductor memory device global and local decoder.

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Any inquiry concerning this communication or earlier communications from the

examiner should be directed to David Lam whose telephone number is 571-272-1782. The

examiner can normally be reached on 6:00 - 4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, David Nelms can be reached on 571-272-1787. The fax phone numbers for the

organization where this application or proceeding is assigned is (703) 872-9306.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR

system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

D. Lam

December 22, 2004

RIMARY EXAMINER

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